



## KEYNOTE SPEAKERS BIOS



**Cary Sneider** is Vice President for Educator Programs at the Museum of Science in Boston, where his current objective is to help schools implement state standards in technology and engineering. He is currently Principal Investigator of the New England Space Science Initiative in Education (NESSIE) with support from NASA, Predicting the Future: The Science and Technology of Weather Forecasting, a museum-school program funded by the National Science Foundation (NSF) and the Institute for Museum and Library Services (IMLS), and Gateway to Technology and Engineering Education, a program for school district leadership teams, also funded by the IMLS. He also heads a project to develop a course for high school students entitled Engineering the Future: Designing the World of the 21st Century, with support from the National Institute for Standards and Technology, the Massachusetts Renewable Energy Trust, Cisco Corporation, and Lockheed-Martin. His publications include teachers' guides for the elementary, middle, and high school levels, articles about the instructional uses of computers, and research studies on how children acquire concepts and skills in science. He helped to develop the National Science Education Standards (National Academy Press, 1996) and contributed to Designing Professional Development Programs for Teachers of Mathematics and Science (Corwin Press, 1998). He currently services on the Advisory Board for the Center for Education at the National Research Council. In 1997 he received the Distinguished Informal Science Education award from NSTA, and in 2003 was named National Associate of the National Academy of Sciences.



**Stephen Pawlowski** is a Senior Fellow and Chief Technology Officer and Director of Platform Planning, Architecture and Technology in the Enterprise Platforms Group. His organization is chartered with planning and designing products that bring Intel-based servers and workstations competitive advantages at both component and platform levels. Pawlowski joined Intel in 1982. He led the design of the first Multibus I Single Board Computer based on the 386 processor. He was a lead architect and designer for Intel's early desktop PC and high performance server products and was the co-architect for Intel's first P6 based server chipsets. He helped define the system bus interfaces for Intel's P6 family processors, the Pentium® 4 processor and Itanium™ processor. He also created and led the research for Intel's agile radio architecture for a future generation of wireless products and prior to his current assignment was the director of Corporate Technology Group's Microprocessor Technology Lab. Pawlowski graduated from the Oregon Institute of Technology in 1982 with bachelor's degrees in electrical engineering technology and computer systems engineering technology, and received a master's degree in computer science and engineering from the Oregon Graduate Institute in 1993. Pawlowski holds 52 patents in the area of system, and microprocessor technologies. He has received three Intel Achievement Awards.